Dolphin Design

IP name	IP partner	IP type	Technology	IP description
SESAME-ICK-HD-DV_TSMC_55nm_uLP-eF_eLVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage eLVT
sDACa-MT1.01	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CLN55LP	1.2V,TSMC 24-bit PWM DAC converter
SESAME-9T-DV_TSMC_55nm_uLP-eF_eLVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,9-Track, dual voltage eLVT
SESAME-ICK-9T-DV_TSMC_55nm_uLP-eF_eLVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage eLVT
SESAME-ICK-9T-DV_TSMC_55nm_uLP-eF_eHVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage eHVT
DpRAM_ERIS_HD-RR_TSMC_65nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN65LP	1.2 V, Dual Port RAM, optimized for ultra High Density and high speed, byte-write, HD Bist
32kx40_SpRAM-PLUTON-eLC-HD-RR_TSMC_180nm_eLL	Dolphin Design	Hard/ Memory/ SRAM Macro	CE018FY	1.8 V & 1.2 V, Single port sRAM instance, optimized for low power consumption and high density with Retention Ready option (eLC-RR) for easy memory power islet construction, optional multi voltage characterizations
mxCOD95.MP01-Helium2-2V5_T65LP	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CLN65LP	1.2V/2.5V,stereo ADC, mono DAC, for high resolution voice CODEC: 95 dB. Low power for portable applications: Mobile phones, DSC, DVC
		Hard/ Mixed-Signal IP/ CODEC	CLN65LP	
		Hard/ Mixed-Signal IP/ ADC	CLN65LP	
shCODlp-97.01_T90LP	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CLN90LP	1.2V/3.3V, Stereo 97 dB audio codec with capless headphone driver
		Hard/ Mixed-Signal IP/ CODEC	CLN90LP	
		Hard/ Mixed-Signal IP/ ADC	CLN90LP	
shCOD90.TP50_T180G_M	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CM018MG	1.8V/3.3V,Stereo 90dB audio codec for smartphones & PMP(CM018G)
		Hard/ Mixed-Signal IP/ ADC	CM018MG	
		Hard/ Mixed-Signal IP/ CODEC	CM018MG	
SESAME_BiV 1.8 - 3.3_TSMC_0.13um_G_SVT	Dolphin Design	Hard/ Standard Cell	CL013G	1.8 V / 3.3 V, 9-Track, Battery Interface Voltage Library, ultra low leakage, voltage from 1.8 V up to 3.3 V
LRO-2.6~4.2-1.8~3.3_T180G	Dolphin Design	Hard/ Analog/ VR	CL018G	From 2.6V to 4.2V input and from 1.8V to 3.3V output, Ultra low noise linear upstream regulator 80 dB PSRR
SESAME_BTF_uHD_LV_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.1 V, 6-Track, High density Reduced Cell Stem Library, High Density Spinner Cell, low voltage
		Hard/ Standard Cell	CM018MG	

SESAME_BTF_uHD_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8 V, 6-Track, High density Reduced Cell Stem Library, High Density Spinner Cell
loDAC-95-LO.pl_T130G	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CL013G	1.2V/3.3V,Stereo 95 dB PWM audio DAC PLL-less
sCOD90-N.01-2G_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ ADC	CM018MG	1.8V/3.3V, Stereo 90 dB audio codec
		Hard/ Mixed-Signal IP/ CODEC	CM018MG	
		Hard/ Mixed-Signal IP/ DAC	CM018MG	
SpRAM-PLUTON-eLC-DV_TSMC_180nm_uLL	Dolphin Design	Hard/ Memory/ SRAM Compiler	CE018FO	1.8V/1.2V,Single port sRAM generator, optimized for low power consumption and high density with Retention Ready option (eLC-RR) for easy memory power islet construction, optional multi voltage characterizations
SESAME-uHD-BTF-NV_TSMC_0.13um_BCD_SVT	Dolphin Design	Hard/ Standard Cell	CV013NI	1.5V/2.5V,1.5 V, 6-Track, Ultra High density Reduced Cell Stem Library
SpRAM-RHEA-NV-LL_TSMC_90nm_uLL	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FT	1.2 V, Single Port RAM generator, optimized for Ultra Low Leakage (source biasing), and High Density, byte-write capability, generator range from 8kb up to 640 kb
mCODa-MT1-LR.01	Dolphin Design	Hard/ Mixed-Signal IP/ ADC	CLN55LP	1.2V/2.5V,TSMC CLN55LP, 24-bit audio CODEC
		Hard/ Mixed-Signal IP/ DAC	CLN55LP	
		Hard/ Mixed-Signal IP/ CODEC	CLN55LP	
1PRFile-AURA-NV-HD-RR_TSMC_90nm_uLL	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2V,Single Port Register File, optimized for Ultra High Density and High Speed, byte-write capability, generator range up to 4kb
SESAME-ICK-9T-DV_TSMC_55nm_uLP_LVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage LVT
SESAME-ICK-HD-DV_TSMC_55nm_uLP_LVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage LVT
SESAME-HD-DV_TSMC_55nm_uLP_eLVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,6-Track, dual voltage eLVT
SESAME-ICK-HD-DV_TSMC_55nm_uLP_eLVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage eLVT
SESAME-ICK-9T-DV_TSMC_55nm_uLP_eLVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage eLVT

DpRAM-ERIS-NV-HD-RR_b-HVT_HVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FS	1.2V,Dual Port RAM, optimized for Ultra High Density and Low Leakage, byte-write capability, generator range from 128 b up
		Hard/ Memory/ SRAM Compiler	CEN90FT	
RAR_eSR_qLR_ref_NV_Iq150_I100_02_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,retention alternative regulator
eSR_Niagara_Bu_dm_ref_NV_I100_01_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
eSR_Niagara_Bu_dm_ref_NV_I100HDO_01_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
eSR_Niagara_Bu_dm_ref_NV_I100_01_CLN55UP_4x1z	Dolphin Design	Hard/ Analog/ VR	CLN55UP	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
eSR_Niagara_Bu_dm_ref_NV_I100uHDO_01_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
OPM_Angel_AR_OV55_01_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Over-Voltage Protection Module
BOR_01_CEN55FL_4x1z1u	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Brown-Out Reset
RAR_eSR_qLR_ref_NV_Iq150_I100HDO_02_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,retention alternative regulator
2PRF-ERA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP	Dolphin Design	Hard/ Memory/ Register File	CLN55UP	1.2V/1.1V/0.9V,Two Port Register File generator, optimized for ultra high density and low power, generator range from 128 to 16 kbits
sCODS100-LB-IO-N.14-Xenon_40nm_4x1z1u	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40LP	1.1V/2.5V,Low Power Audio Codec 24b with stereo differential HP output
sCODa-MT1-LR.01_CLN55LP_4x1z	Dolphin Design	Hard/ Mixed-Signal IP/ ADC	CLN55LP	1.2V/2.5V,TSMC CNL55LP 24-bit audio CODEC with embedded regulator
		Hard/ Mixed-Signal IP/ DAC	CLN55LP	
		Hard/ Mixed-Signal IP/ CODEC	CLN55LP	
sROMet-TITAN-DV_b-SVT_SVT_TSMC_40nm_ULP-eF	Dolphin Design	Hard/ Memory/ ROM	CEN40FL	1.1V/0.9V,Via programmable ROM generator, optimized for Low Power Consumption and High Density, generator range from 1kbits up to 1152kbits
eSR_Niagara_Bu_dm_ref_OV44_I100_02_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
eSR_Niagara_Bu_dm_ref_NV_I200_01_CLN55UP_4x1z	Dolphin Design	Hard/ Analog/ VR	CLN55UP	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
sDSA-VD-LP.02_CEN55FL	Dolphin Design	Hard/ Mixed-Signal IP/ Others	CEN55FL	1.2V,TSMC CEN55FL, digital Mic interface with voice trigger
		Hard/ Mixed-Signal IP/ ADC	CEN55FL	

SESAME-9T-eLV	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/0.9V/0.75V,9T very low voltage library SVT
SESAME-CLICK-DV_TSMC_55nm_uLPeFlash_SVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/0.9V/0.75V,CLICK addon for low voltage library SVT
RAR_eSR_qLR_ref_162_363_055_25_01_CEN55FL	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Retention Alternative Regulator
qLR_Aubrey_ref_162_363_055_25_I1_Iq150_01_CEN55FL	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Ultra low quiescent-current Regulator
nLR_Charny_ref_5V_27_55_08_50_03_I100_CE018FG_5x1z	Dolphin Design	Hard/ Analog/ VR	CE018FG	2.7V/5.5V,Low Noise, High PSRR Linear Regulator
SESAME-ICK-HD-DV_TSMC_55nm_uLP_LVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage LVT
SESAME-ICK-HD-DV_TSMC_55nm_uLP_eLVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage eLVT
iLR_Victoria_ref_NV_I100_02_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Low-Dropout Linear Regulator
SESAME-CLICK-TSMC-55NM_ULPeflash_HVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,CLICK addon for low voltage library SVT
SESAME-CLICK_TSMC_40nm_uLPeF	Dolphin Design	Hard/ Standard Cell	CEN40FL	0.9V/1.1V,Power Gating solution for any island (digital, analog) with 99% leakage
SESAME-BIV_TSMC_90NM_EFLASH_SVT	Dolphin Design	Hard/ Standard Cell	CEN90FP	1.2V/3.3V,1.2 V and 3.3 V Battery Interface Voltage Library, Ultra low leakage
SESAME-ICK-HD-DV_TSMC_55nm_uLP_SVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage SVT
DpRAM-ERA-M1-DV_b-eHVT_SVT-HVT_TSMC_55nm_ULP-eF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN55FL	1.2V/1.1V/0.9V,Dual Port RAM generator, optimized for ultra high density and low power, generator range from 128 to 72 kbits
SESAME-ICK-9T-DV_TSMC_55nm_uLP_SVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage SVT
qCMPH_LP_RR_NV_01_CEN55FL_4x1z	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Low-Quiescent Hysteresis Comparator
POR_BOR_AON_01_CEN55FL_4x1z1u	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Power-On Reset / Brown-Out Reset

SESAME-ICK-9T-DV_TSMC_55nm_uLP-eF_HVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage HVT
RAR_eSR_qLR_ref_NV_Iq150_I100HDO_02_CEN55FL_4x1z1u	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,retention alternative regulator
SESAME-CLICK_TSMC_0.18UM_ELL_SVT	Dolphin Design	Hard/ Standard Cell	CE018FY	1.2V/0.9V/0.75V,CLICK addon for low voltage library SVT
SESAME-ICK-9T-ELV_TSMC_55nm_LP_SVT	Dolphin Design	Hard/ Standard Cell	CLN55LP	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage SVT
sCODS100-IO-N.02-Xenon	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40LP	1.1V/2.5V,tsmc CLN40LP 24-bit audio CODEC
		Hard/ Mixed-Signal IP/ DAC	CLN40LP	
		Hard/ Mixed-Signal IP/ ADC	CLN40LP	
nLR_Charny_ref_5V_20_55_18_50_I100_03_CM018MG_3x1u	Dolphin Design	Hard/ Analog/ VR	CM018MG	2.0V/5.5V,low noise, high PSRR low dropout regulator
SESAME-ICK-HD-DV_TSMC_55nm_uLP-eF_HVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage HVT
SESAME-HD-DV_TSMC_55nm_uLP-eF_HVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,6-Track, dual voltage HVT
SpRAM-RHEA-DV-LL_b-HVT_SVT-HVT_TSMC_90nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN90LP	1.0V/1.2V,Single Port RAM generator, optimized for Ultra Low Leakage (source biasing), and High Density, low voltage capability, write mask capability, generator range from 8kb up to 576kb
SESAME-9T-DV_TSMC_55nm_uLP-eF_HVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,9-Track, dual voltage HVT
SESAME-ICK-HD-DV_TSMC_55nm_uLP-eF_LVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage LVT
SESAME-9T-DV_TSMC_55nm_uLP-eF_LVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,9-Track, dual voltage LVT

LR_HELIUM2_01_40NM-1.1-2.5-CU-EL_1P7M_4X1Y1Z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	1.1V/2.5V,Ultra low noise linear regulator. 2.97 V to 3.63 V input voltage range to 2.5 V output voltage. This product embeds voltage references for use in the codec and MICBIAS output ports.
SESAME-HD-DV_TSMC_55nm_uLP-eF_LVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,6-Track, dual voltage LVT
LR_HELIUM2_01_40NM-1.1-2.5-CU-EL_1P8M_5X2Z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	1.1V/2.5V,Ultra low noise linear regulator. 2.97 V to 3.63 V input voltage range to 2.5 V output voltage. This product embeds voltage references for use in the codec and MICBIAS output ports.
iLR_Mystic_ref_297_363_08_25_I200_01_CLN40LP_5x2z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	2.97V/3.63V,Low Dropout Linear Regulator
SESAME-ICK-9T-DV_TSMC_55nm_uLP-eF_LVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage LVT
SpRAM-VENUS-HD-LP-NV_TSMC_40nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN40LP	1.1V,Single Port RAM generator, optimized for ultra High Density and ultra low power, generator range from 256 bits up to 640 kbits, configurable byte-write, retention mode
RAR_ESR_ILR_1.62-3.63_04	Dolphin Design	Hard/ Analog/ VR	CE018FY	1.62V/3.63V,TSMC 180 eLL RAR_ESR_ILR_1.62-3.63_04
SpRAM-RHEA-NV-HD-RR_TSMC_180nm_uLL-HV	Dolphin Design	Hard/ Memory/ SRAM Compiler	CE018FF	1.8V,Single port RAM generator Single port sRAM generator, optimized for low power consumption and high density
		Hard/ Memory/ SRAM Compiler	CE018FZ	
2PRFile-ERA-TSMC_55nm_HV_SVT-LVT	Dolphin Design	Hard/ Memory/ Register File	CVN55NF	1.2V,Two Port register file generator, optimized for High speed, High Density and low power
qCMPH-2.0-3.63-1.2-1.98-04	Dolphin Design	Hard/ Analog/ Others	CE018FY	2.0V/3.63V,TSMC 180 eLL qCMPH-2.0-3.63-1.2-1.98-04
WT-a-HD.03	Dolphin Design	Hard/ Mixed-Signal IP/ Others	CLN55UP	0.9V/2.5V,Voice Activity Detector
eSR-Niagara-Bu-dm-ref_19_44_08_33_I200_01_T180	Dolphin Design	Hard/ Analog/ VR	CL018G	1.62V/4.4V,Highly Efficient Switching Regulator
SESAME-ICK-9T-eLV_TSMC_55nm_uLP eFlash_SVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/0.9V/0.75V,ICK 9T addon for low voltage library SVT

qCMPH-2.0-3.63-1.2-1.98-02	Dolphin Design	Hard/ Analog/ Others	CE018FY	2.0V/3.63V,TSMC 180 eLL qCMPH-[2.0-3.63]-[1.2-1.98]-02
sROMet-PHOENIX-NV_b_SVT_HVT_TSMC_90_LP	Dolphin Design	Hard/ Memory/ ROM	CLN90LP	1.2 V, Via programmable ROM generator, optimized for low power consumption and high density
POR_BOR_LP_15_363_06_33_03	Dolphin Design	Hard/ Analog/ POR	CEN90FT	3.3V,Power On Reset / Bronw On Reset
ACOMIF16	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CM018MG	1.8V/3.3V, 75dB Voice CODEC 1P5M
		Hard/ Mixed-Signal IP/ DAC	CM018MG	
		Hard/ Mixed-Signal IP/ ADC	CM018MG	
hiDACEm-80Ai/[8-48]_CM018MS-1.8-3.3-AL	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CM018MG	1.8V/3.3V 85dB audio DAC with mixer output - 8 kHz to 48 kHz sampling frequency
SESAME-eLC_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8 V, 7-Track, Patented Extremely Low power Consumption and High Speed standard cell library
sROMet_CASSIOPEIA_LLA_TSMC_130nm_G	Dolphin Design	Hard/ Memory/ ROM	CL013G	1.2 V & 0.9 V, Low-power via programmable ROM generator, optional low voltage
sROMet_CASSIOPEIA_HSL_TSMC_350nm_G	Dolphin Design	Hard/ Memory/ ROM	CM035MG	2.5 V, Ultra Low Power metal ROM instance
SpRAM_PLUTON_XAM_ULP_TSMC_350nm_G	Dolphin Design	Hard/ Memory/ SRAM Compiler	CM035MG	2.5 V, Low Power Single-Port RAM instance
hiCODlv-9001-HD_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CM018MG	1.8V/3.3V, 90 dB audio CODEC with capless headphone driver and complete mixing
		Hard/ Mixed-Signal IP/ DAC	CM018MG	
		Hard/ Mixed-Signal IP/ ADC	CM018MG	
tROMet-CASSIOPEIA_uHDvLC_TSMC_180nm_G	Dolphin Design	Hard/ Memory/ ROM	CL018G	1.8V,Metal programmable ROM instance, optimized for ultra High Density
tROMet-PHOENIX-HD_TSMC_90nm_LP	Dolphin Design	Hard/ Memory/ ROM	CLN90LP	1.2V,Patented Metal programmable ROM instance, optimized for Ultra High Density and Low Leakage
qCMPH-2.0-3.63-1.2-1.98-01	Dolphin Design	Hard/ Analog/ Others	CE018FY	2.0V/3.63V,TSMC 180 eLL qCMPH-[2.0-3.63]-[1.2-1.98]-01
loDAC-95-lo.le_T130G	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CL013G	1.2V/3.3V, PWM Audio DAC Digital Core
shCOD95.SP04-Helium2-2V5_T65LP	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN65LP	1.2V/2.5V, stereo 95dB audio CODEC, ultra low power stereo audio CODEC with LDO
		Hard/ Mixed-Signal IP/ DAC	CLN65LP	
		Hard/ Mixed-Signal IP/ ADC	CLN65LP	
LRO-1.8-3.6_1.25_T65LP	Dolphin Design	Hard/ Analog/ VR	CLN65LP	From 1.8V to 3.6V input and 1.25V output, Ultra low noise linear regulator

mhCOD85.01-2G_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CL018G	1.8V/3.3V, High Quality Audio Mono Codec 85dB (ADC + DAC)
		Hard/ Mixed-Signal IP/ DAC	CL018G	
		Hard/ Mixed-Signal IP/ ADC	CL018G	
shCOD90.BT50_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CL018G	1.8V/3.3V, High Quality Audio Stereo Codec 90dB(ADC + DAC)
		Hard/ Mixed-Signal IP/ DAC	CL018G	
		Hard/ Mixed-Signal IP/ ADC	CL018G	
shCOD90.TP50_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CL018G	1.8V/3.3V, High Quality Audio Stereo Codec 90dB (ADC + DAC)
		Hard/ Mixed-Signal IP/ DAC	CL018G	
		Hard/ Mixed-Signal IP/ ADC	CL018G	
SRI-3.0-3.6_1.8_CL018G	Dolphin Design	Hard/ Analog/ VR	CL018G	From 3.0V to 3.6V input and 1.8V output, Inductor-less buck Switching Regulator
SRI-3.0-3.6_1.0_T180G	Dolphin Design	Hard/ Analog/ VR	CL018G	From 3.0V to 3.6V input and 1.0V output, Inductor-less buck Switching Regulator
SESAME_BiV_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CM018MG	1.8 V and 3.3 V, 9-Track, Battery Interface Voltage Library, ultra low leakage, voltage from 1.8 V up to 3.3 V
		Hard/ Standard Cell	CL018G	
		Hard/ Standard Cell	CE018FZ	
		Hard/ Standard Cell	CE018FY	
		Hard/ Standard Cell	CE018FP	
		Hard/ Standard Cell	CE018FM	
		Hard/ Standard Cell	CE018FG	
		Hard/ Standard Cell	CE018FF	
		Hard/ Standard Cell	CE018FE	
		Hard/ Standard Cell	CE018FD	
		Hard/ Standard Cell	CE018FC	
SESAME-LV-1G_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.1 V, 7-Track, SESAME LV-1G without Level Shifters
shCOD95.SP05-Helium2-2V5_T65LP	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN65LP	1.2V/2.5V, stereo audio CODEC, high resolution 95 dB, low power for portable application: smart phone, DSC, DVC, MP3...
		Hard/ Mixed-Signal IP/ DAC	CLN65LP	
		Hard/ Mixed-Signal IP/ ADC	CLN65LP	
SESAME_BTF_uHD_TSMC_0.13um_G_SVT	Dolphin Design	Hard/ Standard Cell	CL013G	1.2 V, 6-Track, High density Reduced Cell Stem Library, High Density Spinner Cell, nominal voltage

sROMet_PHOENIX_uHDeLL_TSMC_90nm_LP	Dolphin Design	Hard/ Memory/ ROM	CLN90LP	1.2V,Via programmable ROM generator, optimized for low power consumption and high density
		Hard/ Mixed-Signal IP/ CODEC	CL018G	
		Hard/ Mixed-Signal IP/ CODEC	CE018FZ	
		Hard/ Mixed-Signal IP/ CODEC	CE018FY	
		Hard/ Mixed-Signal IP/ CODEC	CE018FS	
		Hard/ Mixed-Signal IP/ CODEC	CE018FP	
		Hard/ Mixed-Signal IP/ CODEC	CE018FM	
		Hard/ Mixed-Signal IP/ CODEC	CE018FG	
		Hard/ Mixed-Signal IP/ CODEC	CE018FF	
		Hard/ Mixed-Signal IP/ CODEC	CE018FE	
		Hard/ Mixed-Signal IP/ CODEC	CE018FD	
		Hard/ Mixed-Signal IP/ CODEC	CE018FC	
		Hard/ Mixed-Signal IP/ CODEC	CE018FB	
		Hard/ Mixed-Signal IP/ DAC	CL018G	
		Hard/ Mixed-Signal IP/ DAC	CE018FZ	
		Hard/ Mixed-Signal IP/ DAC	CE018FY	
		Hard/ Mixed-Signal IP/ DAC	CE018FS	
		Hard/ Mixed-Signal IP/ DAC	CE018FP	
shCODlv-90.14-2G_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CE018FM	1.8V/3.3V, 90 dB audio CODEC with capacitor-less headphone driver and analog mixing
		Hard/ Mixed-Signal IP/ DAC	CE018FG	
		Hard/ Mixed-Signal IP/ DAC	CE018FF	
		Hard/ Mixed-Signal IP/ DAC	CE018FE	
		Hard/ Mixed-Signal IP/ DAC	CE018FD	
		Hard/ Mixed-Signal IP/ DAC	CE018FC	
		Hard/ Mixed-Signal IP/ DAC	CE018FB	
		Hard/ Mixed-Signal IP/ ADC	CL018G	
		Hard/ Mixed-Signal IP/ ADC	CE018FZ	
		Hard/ Mixed-Signal IP/ ADC	CE018FY	
		Hard/ Mixed-Signal IP/ ADC	CE018FS	
		Hard/ Mixed-Signal IP/ ADC	CE018FP	
		Hard/ Mixed-Signal IP/ ADC	CE018FM	

		Hard/ Mixed-Signal IP/ ADC	CE018FG	
		Hard/ Mixed-Signal IP/ ADC	CE018FF	
		Hard/ Mixed-Signal IP/ ADC	CE018FE	
		Hard/ Mixed-Signal IP/ ADC	CE018FD	
		Hard/ Mixed-Signal IP/ ADC	CE018FC	
		Hard/ Mixed-Signal IP/ ADC	CE018FB	
dROMet_CASSIOPEIA_eHDeLC_TSMC_180nm_G_M4_UTM	Dolphin Design	Hard/ Memory/ ROM	CL018G	1.8V,Metal programmable ROM instances, optimized for Low Power Consumption and High Density
sROMet_PHOENIX_uHDeLL_TSMC_90nm_G	Dolphin Design	Hard/ Memory/ ROM	CLN90G	1.0V,Via programmable ROM generator, optimized for low power consumption and high density
sensAFE-20x1-PS.01_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ AFE	CM018MG	1.8V/3.3V, 20 bit High Resolution Analog Front-End (voltage mode) for capacitive MEMS (pressure sensor)
sensAFE-20-AS.01-CL_CL018G	Dolphin Design	Hard/ Analog/ Others	CM018MG	1.8V/3.3V,High Resolution Closed Loop Converter for capacitive MEMS
sensAFE-21x2-PS.01_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ AFE	CM018MG	1.8V/3.3V, 21 bit High Resolution Analog Front-End (charge mode) for capacitive MEMS (pressure sensor)
shCODlv-90.03-HD_T180G	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CM018MG	1.8V/3.3V, High Quality Audio Stereo Codec (ADC+DAC)
		Hard/ Mixed-Signal IP/ DAC	CM018MG	
		Hard/ Mixed-Signal IP/ ADC	CM018MG	
1PRFile_AURA_BDS_TSMC_130nm_G	Dolphin Design	Hard/ Memory/ Register File	CL013G	1.2 V & 0.9 V, Single Port Register File, optimized for ultra High Density and high speed, low voltage capability, optional add-Ons (byte-write, HD Bist)
SESAME-uHD-BTF-DNW_TSMC_0.18um_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8V,1.1V, 6-Track, High Density reduced cell stem standard cell library
SESAME-eHSvHD_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8 V, 6-Track, Extremely High Speed and very High Density standard cell library
SESAME-uHDvLC_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8 V, 6-Track, Ultra High Density and very Low Consumption standard cell library
		Hard/ Standard Cell	CE018FF	
SESAME-BTF-HD_TSMC_0.18um_GP_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8 V, 6-Track, High Density standard cell library with Back Tracking Freedom feature (Standard Vt, SVT, only)
		Hard/ Mixed-Signal IP/ CODEC	CLN40LP	1.1V/2.5V, 2.5V overdrive 3.3V,

sCODS100-N.01-Xenon_T40LP	Dolphin Design	Hard/ Mixed-Signal IP/ DAC	CLN40LP	stereo CODEC, 100 dB of SNR, Low Power, with its own regulator
		Hard/ Mixed-Signal IP/ ADC	CLN40LP	
sensAFE-15x1-PM.01_CE018G	Dolphin Design	Hard/ Mixed-Signal IP/ AFE	CE018FZ	1.8V/3.3V, 15 bits Sensor Analog-Front-End, for Voltage Path in Power Metering application
		Hard/ Mixed-Signal IP/ AFE	CE018FY	
		Hard/ Mixed-Signal IP/ AFE	CE018FS	
		Hard/ Mixed-Signal IP/ AFE	CE018FP	
		Hard/ Mixed-Signal IP/ AFE	CE018FM	
		Hard/ Mixed-Signal IP/ AFE	CE018FG	
		Hard/ Mixed-Signal IP/ AFE	CE018FF	
		Hard/ Mixed-Signal IP/ AFE	CE018FE	
		Hard/ Mixed-Signal IP/ AFE	CE018FD	
		Hard/ Mixed-Signal IP/ AFE	CE018FC	
		Hard/ Mixed-Signal IP/ AFE	CE018FB	
sensAFE-20x1-PM.01_CE018G	Dolphin Design	Hard/ Mixed-Signal IP/ AFE	CE018FZ	1.8V/3.3V, 20 bits Sensor Analog-Front-End , for Current & Tampering Path in Power Metering application
		Hard/ Mixed-Signal IP/ AFE	CE018FY	
		Hard/ Mixed-Signal IP/ AFE	CE018FS	
		Hard/ Mixed-Signal IP/ AFE	CE018FP	
		Hard/ Mixed-Signal IP/ AFE	CE018FM	
		Hard/ Mixed-Signal IP/ AFE	CE018FG	
		Hard/ Mixed-Signal IP/ AFE	CE018FF	
		Hard/ Mixed-Signal IP/ AFE	CE018FE	
		Hard/ Mixed-Signal IP/ AFE	CE018FD	
		Hard/ Mixed-Signal IP/ AFE	CE018FC	
		Hard/ Mixed-Signal IP/ AFE	CE018FB	
LRA-2.97-3.63_1.8_CE018G	Dolphin Design	Hard/ Analog/ VR	CE018FZ	From 2.97V to 3.63V input and 1.8V output, I/O 3.3V, low noise linear regulator
		Hard/ Analog/ VR	CE018FY	
		Hard/ Analog/ VR	CE018FS	
		Hard/ Analog/ VR	CE018FP	
		Hard/ Analog/ VR	CE018FM	
		Hard/ Analog/ VR	CE018FG	
		Hard/ Analog/ VR	CE018FF	
		Hard/ Analog/ VR	CE018FE	
		Hard/ Analog/ VR	CE018FD	
		Hard/ Analog/ VR	CE018FC	
		Hard/ Analog/ VR	CE018FB	
		Hard/ Mixed-Signal IP/ ADC	CE018FE	
		Hard/ Mixed-Signal IP/ ADC	CE018FD	

auxADC.01_CE018G	Dolphin Design	Hard/ Mixed-Signal IP/ ADC	CE018FC	1.8V/3.3V, auxiliary ADC for measure applications
		Hard/ Mixed-Signal IP/ ADC	CE018FB	
		Hard/ Mixed-Signal IP/ ADC	CE018FZ	
		Hard/ Mixed-Signal IP/ ADC	CE018FY	
		Hard/ Mixed-Signal IP/ ADC	CE018FS	
		Hard/ Mixed-Signal IP/ ADC	CE018FP	
		Hard/ Mixed-Signal IP/ ADC	CE018FM	
		Hard/ Mixed-Signal IP/ ADC	CE018FG	
		Hard/ Mixed-Signal IP/ ADC	CE018FF	
VRef.LN01_CE018G	Dolphin Design	Hard/ Mixed-Signal IP/ AFE	CE018FZ	1.8V/3.3V, reference Voltage presenting low temperature drift and low noise for power metering applications
		Hard/ Mixed-Signal IP/ AFE	CE018FY	
		Hard/ Mixed-Signal IP/ AFE	CE018FS	
		Hard/ Mixed-Signal IP/ AFE	CE018FP	
		Hard/ Mixed-Signal IP/ AFE	CE018FM	
		Hard/ Mixed-Signal IP/ AFE	CE018FG	
		Hard/ Mixed-Signal IP/ AFE	CE018FF	
		Hard/ Mixed-Signal IP/ AFE	CE018FE	
		Hard/ Mixed-Signal IP/ AFE	CE018FD	
		Hard/ Mixed-Signal IP/ AFE	CE018FC	
		Hard/ Mixed-Signal IP/ AFE	CE018FB	
OPM-Hydra-ref-1.9-4.4-4-q.02	Dolphin Design	Hard/ Analog/ VR	CE018FY	1.9V/4.4V,TSMC 180 eLL RAR-OPM-Angel-ref-[1.9-4.4]-4q.02
SpRAM-RHEA-HD-RR_b-HVT_SVT-HVT_TSMC_55nm_ULP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN55UP	1.2V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 8 kbits up to 320 kbits, configurable byte-write, retention mode.
eSR_Niagara_Bu_dm_ref_NV_I500HDO_01_CLN40LP_4x2z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	1.62V/3.63V,Buck Switching Inductor Regulator
RAR_eSR_qLR_ref_NV_Iq150_I100HDO_02_CLN55LP_6x2z	Dolphin Design	Hard/ Analog/ VR	CLN55LP	1.62V/3.63V,Retention Alternating Regulator
SESAME-9T-DV_TSMC_55nm_uLP_HVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,9-Track, dual voltage HVT

SESAME-ICK-HD-DV_TSMC_55nm_uLP_SVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage SVT
iLR_Victoria_ref_NV_I100_02_CLN40LP_5x1z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	1.62V/3.63V,Low-Dropout Linear Regulator
shCOD90.15-2G	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CL018G	1.8V/3.3 V, low BoM CODEC: mono ADC, stero DAC with 90 dB of SNR,
		Hard/ Mixed-Signal IP/ DAC	CL018G	
		Hard/ Mixed-Signal IP/ ADC	CL018G	
ACU-PMN-A.02	Dolphin Design	Hard/ Analog/ Others	CE018FY	1.8V/3.63V,TSMC 180 eLL ACU-PMN-A.02
sCODa-MT1-VD-LR.06_CLN28HT_5x2z	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN28HT	0.9V/1.8V,TSMC CLN28HT 24-bit audio CODEC with embedded regulator
sCODa95-H1-LR.08	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN55UP	1.2V/2.5V,Audio Codec with 8 ADC channels and 2 DAC channels, with WhisperTrigger
2pRF-ERIS-NV-HD-RR_b-HVT_SVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2V,Two Port Register File, optimized for Ultra High Density and High Speed, byte-write capability, generator range from
		Hard/ Memory/ Register File	CEN90FS	
SESAME-9T-DV_TSMC_55nm_uLP-eF_eHVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,9-Track, dual voltage eHVT
SESAME-ICK-9T-DV_TSMC_55nm_uLP_eHVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage eHVT
SpRAM-PLUTON-eLC_TSMC_180nm_BCD_GENIII	Dolphin Design	Hard/ Memory/ SRAM Compiler	CV018M1	1.8V,Single port sRAM generator, optimized for low power consumption and high density
SpRAM-RHEA-HD-RR-NV_TSMC_65nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN65LP	1.2V,Single Port RAM generator, optimized for ultra High Density and high speed, generator range from 256 bits up to 320 kbits, configurable byte-write, retention mode
SESAME-HD-DV_TSMC_55nm_uLP_LVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,6-Track, dual voltage LVT
SESAME-9T-DV_TSMC_55nm_uLP_eHVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,9-Track, dual voltage eHVT
SESAME-9T-DV_TSMC_55nm_uLP_LVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,9-Track, dual voltage LVT

dROMet_CASSIOPEIA_LL_TSMC_180nm_ELL	Dolphin Design	Hard/ Memory/ ROM	CE018FY	1.8V/1.2V,Metal programmable ROM generator, optimized for low power consumption and high density, optional multi voltage characterizations
SESAME-ICK-HD-DV_TSMC_55nm_uLP_HVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage HVT
eSR_Niagara_Bu_dm_ref_NV_I200_01_CLN40LP_5x1z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
eSR_Niagara_Bu_dmref_NV_I100HDO_01_CEN55FL_4x1z1uA	Dolphin Design	Hard/ Analog/ VR	CEN55FL	1.62V/3.63V,Inductor-based buck switching regulator with high efficiency
2PRFile-ERA-TSMC_55nm_HV_SVT	Dolphin Design	Hard/ Memory/ Register File	CVN55NF	1.2V,Two Port register file generator, optimized for High speed, High Density and low power
SESAME-ICK-HD-DV_TSMC_55nm_uLP_HVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage HVT
SESAME-ICK-9T-DV_TSMC_55nm_uLP_HVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage HVT
SESAME-9T-DV_TSMC_55nm_uLP-eF_SVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,9-Track, dual voltage SVT
SESAME-ICK-9T-DV_TSMC_55nm_uLP-eF_SVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 9-Track, dual voltage SVT
DpRAM-ERA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN55UP	1.2V/1.1V/0.9V,Dual Port RAM generator, optimized for ultra high density and low power, generator range from 128 to 64 kbits
2PRF-ERA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP-eF	Dolphin Design	Hard/ Memory/ Register File	CEN55FL	1.2V/1.1V/0.9V,Two Port Register File generator, optimized for ultra high density and low power, generator range from 128 to 16 kbits
DpRAM-ERA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP-eF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN55FL	1.2V/1.1V/0.9V,Dual Port RAM generator, optimized for ultra high density and low power, generator range from 128 to 64 kbits
SpRAM-LYRA-NV-HD-b-SVT_SVT_TSMC_55nm_HV	Dolphin Design	Hard/ Memory/ SRAM Compiler	CVN55NF	1.2V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 512 bits up to 320 kbits

SpRAM-LYRA-NV-HD-b-SVT_LVT-SVT_TSMC_55nm_HV	Dolphin Design	Hard/ Memory/ SRAM Compiler	CVN55NF	1.2V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 512 bits up to 320 kbits
eSR-Niagara-Bu-ref-19_44_08_33_I100_01_T180	Dolphin Design	Hard/ Analog/ VR	CL018G	1.62V/4.4V,Highly Efficient Switching Regulator
SESAME-eLC-DV_TSMC_0.18UM_BCD_SVT	Dolphin Design	Hard/ Standard Cell	CV018LG	1.8 V and 1.1 V , 7-Track, Patented Extremely Low power Consumption and High Speed standard cell library
		Hard/ Standard Cell	CV018LH	
		Hard/ Standard Cell	CV018M1	
SCODS100-LB-IO-N.15	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40LP	1.1V/3.3V,stereo codec with low-power mode on ADC path
SESAME-ICK-HD-DV_TSMC_55nm_uLP_eHVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage eHVT
1PRFile-AURA-BDS_TSMC_55nm_LP	Dolphin Design	Hard/ Memory/ Register File	CMN55RH	1.2V,Single Port register file generator, optimized for ultra High Density and high speed, generator range from 64 bits up to 40 kbits, byte-write, retention mode
		Hard/ Memory/ Register File	CLN55LP	
		Hard/ Memory/ Register File	CMN55RP	
SROMET_LCL_TSMC130nm	Dolphin Design	Hard/ Memory/ ROM	CL013G	1.2 V, Metal programmable ROM generator, optimized for Low Power Consumption
1pRF-AURA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP	Dolphin Design	Hard/ Memory/ Register File	CLN55UP	1.2V/1.1V/0.9V,One port register file generator, optimized for ultra High Density and low power, generator range from 64 bits up to 40 kbits
SESAME-ICK-HD-DV_TSMC_55nm_uLP_eHVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage eHVT
SpRAM-RHEA-NV-HD-RR_b-SVT_SVT_TSMC_180nm_BCDGenII	Dolphin Design	Hard/ Memory/ SRAM Compiler	CV018MH	1.8V,Single port RAM generator Single port sRAM generator, optimized for low power consumption and high density
qLR-1.62-3.63-1.2-3.3	Dolphin Design	Hard/ Analog/ VR	CE018FY	1.8V/3.3V Ultra low quiescent Linear Regulator
SESAME-HD-DV_TSMC_55nm_uLP_eHVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,6-Track, dual voltage eHVT
1536x36m8S1WM9_SpRAM-RHEA-NV-RR_HVT_TSMC_90nm_LPeF	Dolphin Design	Hard/ Memory/ SRAM Macro	CEN90FT	1.2V,Single Port RAM instance of 1536x36m8 with Write Mask 9
		Hard/ Memory/ SRAM Macro	CEN90FS	
sCOD100-uLP-H1-LR-D.02	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40UP	0.9V/3.3V,Low power Audio Codec with SNR optimizer on ADC

SESAME-BIV_TSMC_40nm_LP_25od33	Dolphin Design	Hard/ Standard Cell	CLN40LP	1.1V/3.3V,1.1 V and 2.5 V overdriven to 3.3 V, Battery Interface Voltage Library, ultra low leakage
		Hard/ Standard Cell	CLN40UP	
SESAME-CLICK_TSMC_40nm_uLP_HVT	Dolphin Design	Hard/ Standard Cell	CLN40UP	0.9V/1.1V,Power Gating solution for any island (digital, analog) with 99% leakage reduction
POR_BOR_162_363_055_33_02_CLN40UP_5x1z	Dolphin Design	Hard/ Analog/ POR	CLN40UP	3.3V,Power On Reset
RAR_eSR_qLR_ref_NV_Iq150_I100_04_CLN40UP_5x1z	Dolphin Design	Hard/ Analog/ VR	CLN40UP	1.62V/3.63V,Retention Alternative Regulator
sCOD100-uLP-ANC-H1-LR-D.01_40uLP_5x1z	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40UP	0.8V/3.3V,TSMC40 uLP Codec with 4-channel ADC and stereo DAC, low latency filters
SpRAM-RHEA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN55UP	1.2V/1.1V/0.9V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 256 bits up to 320 kbits
sADC-H1-LR.02	Dolphin Design	Hard/ Mixed-Signal IP/ ADC	CLN28HT	0.9V/1.8V,4 channels, 16-to-24 bits ADC
LRA-2.7-3.6_2.5-SP04_CLN65LP	Dolphin Design	Hard/ Analog/ VR	CLN65LP	From 2.7V to 3.63V input and 2.5V output, Ultra low noise linear regulator
SpRAM-RHEA-NV-HD-RR_b-HVT_SVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FT	1.2V,Single Port RAM generator, optimized for High Density and Low Power, byte-write capability, generator range from 8kb up to
		Hard/ Memory/ SRAM Compiler	CEN90FS	
sCODS100-LB-IO-N.12	Dolphin Design	Hard/ Mixed-Signal IP/ ADC	CLN40LP	1.1V/2.5V,TSMC CLN40LP 24-bit audio CODEC
		Hard/ Mixed-Signal IP/ DAC	CLN40LP	
		Hard/ Mixed-Signal IP/ CODEC	CLN40LP	
1pRF-AURA-NV-HD-RR_b-HVT_SVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2V,Single Port Register File, optimized for Ultra High Density and High Speed, byte-write capability, generator range from
		Hard/ Memory/ Register File	CEN90FS	
SESAME-9T-DV_TSMC_55nm_uLP_eLVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,9-Track, dual voltage eLVT
SESAME-HD-DV_TSMC_55nm_uLP-eF_eHVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,6-Track, dual voltage eHVT
SESAME_BTF_uHD_DV_TSMC_180nm_G_SVT	Dolphin Design	Hard/ Standard Cell	CL018G	1.8 V and 1.1 V, 6-Track, High density Reduced Cell Stem Library, High Density Spinner Cell, dual voltage: 1.8 V and 1.1
		Hard/ Standard Cell	CM018MG	
sROMet_HAUMEA_BCD_TSMC_65nm_LP	Dolphin Design	Hard/ Memory/ ROM	CLN65LP	1.2V,Via programmable ROM generator, optimized for low power consumption and high density
		Hard/ Memory/ ROM	CMN65RP	

1pRF-AURA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULP-eF	Dolphin Design	Hard/ Memory/ Register File	CEN55FL	1.2V/1.1V/0.9V,One port register file generator, optimized for ultra High Density and low power, generator range from 64 bits up to 40 kbits(Optional features: Embedded power switch and characterization at 1.0V)
sCODS100-LB-IO-N.12	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40LP	1.1V/2.5V,TSMC CLN40LP 24-bit audio CODEC
		Hard/ Mixed-Signal IP/ DAC	CLN40LP	
		Hard/ Mixed-Signal IP/ ADC	CLN40LP	
SESAME-ICK-HD-DV_TSMC_55nm_uLP-eF_eHVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage eHVT
2pRF-ERIS-NV-HD-RR_b-HVT_HVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2V,Two Port Register File, optimized for Ultra High Density and Low Leakage, byte-write capability, generator range from
		Hard/ Memory/ Register File	CEN90FS	
sROMet-TITAN-DV_b-SVT_SVT_TSMC_55nm_ULP-eF	Dolphin Design	Hard/ Memory/ ROM	CEN55FL	1.2V/1.1V/0.9V,Via programmable ROM generator, optimized for Low Power Consumption and High Density, generator range from 512 bits up to 0.5 Mbits
sROMet-PHOENIX-NV_b-SVT_HVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ ROM	CEN90FT	1.2V,Via programmable ROM generator, optimized for low power consumption and high density, generator range from 16
		Hard/ Memory/ ROM	CEN90FS	
SESAME-HD-DV_TSMC_55nm_uLP_HVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,6-Track, dual voltage HVT
SESAME-HD-DV_TSMC_55nm_uLP_SVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,6-Track, dual voltage SVT
SESAME-9T-DV_TSMC_55nm_uLP_SVT	Dolphin Design	Hard/ Standard Cell	CLN55UP	1.2V/1.1V/0.9V,9-Track, dual voltage SVT
sCODa-MT1-LR.01	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN65LP	1.2V/3.3V,TSMC CLN65LP 24-bit audio CODEC with embedded regulator
1PRF-AURA-NV-HD-RR_TSMC_110nm_HV32	Dolphin Design	Hard/ Memory/ Register File	CV011NF	1.5V,Single Port register file generator, optimized for ultra High Density and high speed, generator range from 128 bits up to 20 kbits, byte-write, retention mode
SpRAM-RHEA-HD-RR_TSMC_110nm_HV32	Dolphin Design	Hard/ Memory/ SRAM Compiler	CV011NF	1.5V,Single Port RAM generator, optimized for ultra High Density, generator range from 8 kbits up to 512 kbits, byte-write, retention mode

16kx32_ERS_SpRAM-PLUTON-eLC-HD-RR_TSMC_180nm_eLL	Dolphin Design	Hard/ Memory/ SRAM Macro	CE018FY	1.8 V & 1.2 V, Single port RAM instance, optimized for low power consumption and high density with power switch option (ERS), optional multi voltage characterizations
sROMet-TITAN-DV_b-SVT_SVT_TSMC_55nm_ULP	Dolphin Design	Hard/ Memory/ ROM	CLN55UP	1.2V/1.1V/0.9V,Via programmable ROM generator, optimized for Low Power Consumption and High Density, generator range from 512 bits up to 0.5 Mbits
LR_HELIUM2_01_40NM-1.1-2.5-CU-EL_1P7M_5X1Z	Dolphin Design	Hard/ Analog/ VR	CLN40LP	1.1V/2.5V,Ultra low noise linear regulator. 2.97 V to 3.63 V input voltage range to 2.5 V output voltage. This product embeds voltage references for use in the codec and MICBIAS output ports.
sROMet-TITAN-TSMC_55nm_HV	Dolphin Design	Hard/ Memory/ ROM	CVN55NF	1.2V,Via programmable ROM generator, optimized for High Density and high speed
SpRAM-RHEA-HD-RR_TSMC_55nm_HV_SVT-LVT	Dolphin Design	Hard/ Memory/ SRAM Compiler	CVN55NF	1.2V,Single Port RAM generator, optimized for ultra High Density and high speed, generator range from 256 bits up to 640 kbits
SESAME-HD-DV_TSMC_55nm_uLP-eF_SVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,6-Track, dual voltage SVT
1PRFile-AURA-DV-HD-RR_b-uLL_SVT-HVT_TSMC_90nm_uLL	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2 V and 1.0 V, Single Port Register File, optimized for Ultra High Density and High Speed, low voltage capability, byte-write capability, generator range up to 4kb
SpRAM-RHEA-DV-LL_b-uLL_SVT-HVT_TSMC_90nm_uLL	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FT	1.2V/1.0V,Single Port RAM generator, optimized for Ultra Low Leakage (source biasing), and High Density, low voltage capability, write mask capability, generator range from 8kb up to 640 kb
SESAME-ICK-HD-DV_TSMC_55nm_uLP-eF_SVT	Dolphin Design	Hard/ Standard Cell	CEN55FL	1.2V/1.1V/0.9V,Island construction kit, 6-Track, dual voltage SVT

SpRAM-RHEA-DV-HD-RR_b-eHVT_SVT-HVT_TSMC_55nm_ULPeF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN55FL	1.2V/1.0V/1.1V/0.9V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 256 bits up to 320 kbits. (Optional features: SpRAM ERS: Embedded power switch and characterization at 1.0V)
SpRAM-RHEA-DV-LL_b-HVT_HVT_TSMC_90nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN90LP	1.0V/1.2V,Single Port RAM generator, optimized for Ultra Low Leakage (source biasing), and High Density, low voltage capability, byte-write capability, generator range from 8kb up to 576kb
SpRAM-RHEA-DV-HD-RR_b-HVT_SVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FT	1.2V/1.0V,Single Port RAM generator, optimized for High Density and Low Power, write mask capability, generator range
		Hard/ Memory/ SRAM Compiler	CEN90FS	
spRAM_NEPTUNE_XAM_LLA_TSMC_180nm_G	Dolphin Design	Hard/ Memory/ SRAM Compiler	CL018G	1.8 V, Patented Single port sRAM generator, optimized for ultra low leakage
		Hard/ Memory/ SRAM Compiler	CE018FZ	
		Hard/ Memory/ SRAM Compiler	CE018FY	
		Hard/ Memory/ SRAM Compiler	CE018FS	
		Hard/ Memory/ SRAM Compiler	CE018FP	
		Hard/ Memory/ SRAM Compiler	CE018FM	
		Hard/ Memory/ SRAM Compiler	CE018FG	
		Hard/ Memory/ SRAM Compiler	CE018FF	
		Hard/ Memory/ SRAM Compiler	CE018FE	
		Hard/ Memory/ SRAM Compiler	CE018FD	
		Hard/ Memory/ SRAM Compiler	CE018FC	
		Hard/ Memory/ SRAM Compiler	CE018FB	
SpRAM-PLUTON-eLC-HD-RR_TSMC_180nm_eLL	Dolphin Design	Hard/ Memory/ SRAM Compiler	CE018FY	1.8V/1.2V,Single port sRAM generator, optimized for low power consumption and high density with Retention Ready option (eLC-RR) for easy memory power islet construction, optional multi voltage characterizations
SpRAM-AURA_TSMC_130nm_BCD	Dolphin Design	Hard/ Memory/ Register File	CV013NI	1.5V,Single Port register file generator, optimized for ultra High Density and high speed, generator range from 128 bits up to 64 kbits, Write Mask option

SpRAM-RHEA-DV-LL_TSMC_90nm_uLL	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FT	1.2V/1.0V,Single Port RAM generator, optimized for Ultra Low Leakage (source biasing), and High Density, low voltage capability, byte-write capability, generator range from 8kb up to 640 kb
dROMet_CASSIOPEIA_eHDeLC_TSMC_180nm_G	Dolphin Design	Hard/ Memory/ ROM	CV018MS	1.8 V, Low-power via/metal programmable ROM generator
		Hard/ Memory/ ROM	CL018G	
		Hard/ Memory/ ROM	CE018FZ	
		Hard/ Memory/ ROM	CE018FY	
		Hard/ Memory/ ROM	CE018FS	
		Hard/ Memory/ ROM	CE018FP	
		Hard/ Memory/ ROM	CE018FM	
		Hard/ Memory/ ROM	CE018FG	
		Hard/ Memory/ ROM	CE018FF	
		Hard/ Memory/ ROM	CE018FE	
		Hard/ Memory/ ROM	CE018FD	
		Hard/ Memory/ ROM	CE018FC	
		Hard/ Memory/ ROM	CE018FB	
spRAM_PLUTON_eLCvHD_TSMC_180nm_G	Dolphin Design	Hard/ Memory/ SRAM Compiler	CV018MS	1.8V/1.1V,Single port sRAM generator, optimized for low power consumption and high density, optional low voltage
		Hard/ Memory/ SRAM Compiler	CL018G	
		Hard/ Memory/ SRAM Compiler	CE018FZ	
		Hard/ Memory/ SRAM Compiler	CE018FY	
		Hard/ Memory/ SRAM Compiler	CE018FP	
		Hard/ Memory/ SRAM Compiler	CE018FM	
		Hard/ Memory/ SRAM Compiler	CE018FG	
		Hard/ Memory/ SRAM Compiler	CE018FF	
		Hard/ Memory/ SRAM Compiler	CE018FE	
		Hard/ Memory/ SRAM Compiler	CE018FD	
		Hard/ Memory/ SRAM Compiler	CE018FC	
DpRAM-ERIS-NV-HD-RR_b-HVT_SVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ SRAM Compiler	CEN90FT	1.2V,Dual Port RAM, optimized for Ultra High Density and High Speed, byte-write capability, generator range from 128 b up
		Hard/ Memory/ SRAM Compiler	CEN90FS	

SpRAM-RHEA-HD-RR-NV_TSMC_55nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN55LP	1.2V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 256 bits up to 320 kbits, configurable byte-write, retention mode. (Optional features: Embedded power switches)
sCODS100-N.11-Xenon	Dolphin Design	Hard/ Mixed-Signal IP/ CODEC	CLN40LP	1.1V/2.5V,TSMC CLN40LP 24-bit audio multi-channel CODEC
DpRAM-ERA-DR_b-LP-SVT_SVT_TSMC_40nm_uLP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN40UP	0.9V/1.1V,Dual Port RAM generator, optimized for ultra high density and low power, generator range from 64 to 288 kbits
spRAM_PLUTON_eLCvHD_TSMC_180nm_G	Dolphin Design	Hard/ Memory/ SRAM Compiler	CE018FZ	1.8V/1.1V,Single port sRAM generator, optimized for low power consumption and high density, optional low voltage
		Hard/ Memory/ SRAM Compiler	CE018FY	
		Hard/ Memory/ SRAM Compiler	CL018G	
		Hard/ Memory/ SRAM Compiler	CE018FP	
		Hard/ Memory/ SRAM Compiler	CE018FM	
		Hard/ Memory/ SRAM Compiler	CV018MS	
		Hard/ Memory/ SRAM Compiler	CE018FD	
		Hard/ Memory/ SRAM Compiler	CE018FC	
		Hard/ Memory/ SRAM Compiler	CE018FG	
		Hard/ Memory/ SRAM Compiler	CE018FF	
		Hard/ Memory/ SRAM Compiler	CE018FE	
SESAME-ICK-HD-DV_TSMC_0.18um_eLL_SVT	Dolphin Design	Hard/ Standard Cell	CE018FY	1.8 V and 1.2 V, Islet Contruction Kit for smooth construction and integration of multi voltages, extinction and retention logic islets allowing a drastic reduction of power
SESAME-HD-DV_TSMC_0.18um_eLL_SVT	Dolphin Design	Hard/ Standard Cell	CE018FY	1.8 V and 1.2 V, 6-Track, High density, Low Leakage Reduced Cell Stem Library, multi voltage characterization
SpRAM-RHEA-HD-ES-DNW_b-HVT_SVT-HVT_TSMC_55nm_LP	Dolphin Design	Hard/ Memory/ SRAM Compiler	CLN55LP	1.2V,Single Port RAM generator, optimized for ultra High Density and low power, generator range from 256 bits up to 320 kbits with Deep Nwell Layer, configurable byte-write and embedded power switch (ES : Extinction Switch)

1PRFile-AURA-DV-HD-RR_TSMC_90nm_uLL	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2 V and 1.0 V, Single Port Register File, optimized for Ultra High Density and High Speed, low voltage capability, byte-write capability, generator range up to 4kb
SpRAM-RHEA-HD-RR_TSMC_55nm_HV_SVT	Dolphin Design	Hard/ Memory/ SRAM Compiler	CVN55NF	1.2V,Single Port RAM generator, optimized for ultra High Density and high speed, generator range from 256 bits up to 640 kbits
1pRF-AURA-DV-HD-RR_b-HVT_SVT_TSMC_90nm_eF	Dolphin Design	Hard/ Memory/ Register File	CEN90FT	1.2V/1.0V,Single Port Register File, optimized for Ultra High Density and High Speed, byte-write capability, generator range
		Hard/ Memory/ Register File	CEN90FS	
SESAME-ELC_TSMC_0.18UM_RF_SVT	Dolphin Design	Hard/ Standard Cell	CM018RI	1.8V/1.1V/0.9V/0.67V,7-Track, Patented Extremely Low power Consumption and High Speed standard cell library
sROMet-CASSIOPEIA-LLA_TSMC_130nm_BCD	Dolphin Design	Hard/ Memory/ ROM	CV013NI	1.5V,Via programmable ROM generator, optimized for Low Power Consumption and High Density